

Amendment  
Serial No. 09/623,643

Docket No. PHD 99-001

**IN THE CLAIMS:**

Please amend the claims as follows:

- B1
1. (Currently Amended) A mobile data carrier, ~~particularly a chip card,~~  
comprising:  
\_\_\_\_\_ a data-processing circuit; and  
\_\_\_\_\_ a supply unit ~~for~~ (1) to applying electric energy to power supply terminals  
for operating the data-processing circuit from an external energy source and (2)  
controlling voltage and current at external access points of the data carrier arranged  
outside the data carrier, wherein the supply unit comprising including  
\_\_\_\_\_ a voltage-limiting control circuit ~~which is arranged in parallel to~~  
the power supply terminals of the data-processing circuit, and  
\_\_\_\_\_ a current control device which, with respect to the supply of  
energy to the data-processing circuit, is arranged in series with the parallel arrangement  
of the voltage-limiting control circuit and the data-processing circuit.
  2. (Currently Amended) A mobile data carrier as claimed in claim 1,  
~~characterized in that~~ wherein the current control device is adapted to supply an at least  
substantially load-independent supply current, and in that the voltage-limiting control  
circuit is adapted to take up an excess current which, with respect to the power supply  
current, is complementary to a power supply current taken up by the data-processing  
circuit at the power supply terminals.
  3. (Currently Amended) A mobile data carrier as claimed in claim 1,  
~~characterized in that~~ wherein the supply current supplied by the current control device is  
at least substantially controlled in dependence upon only one voltage supplied by the  
external energy source.

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4. (Currently Amended) A mobile data carrier as claimed in claim 1, ~~characterized in that~~ wherein the data-processing circuit is implemented with asynchronously operating logic elements whose signal-processing rate is dependent on a power supply voltage applied to the power supply terminals of the data-processing circuit.

31 5. (New) The mobile data carrier as claimed in claim 1, wherein the data-processing circuit is implemented with asynchronously operating logic elements having a signal-processing rate that is dependent on a power supply voltage applied to the power supply terminals.

6. (New) The mobile data carrier as claimed in claim 1, wherein the voltage and current at external access points of the data carrier are constant during operation of the data carrier.